

**Amendments to the Specification**

Please replace paragraph beginning on Line 23 of Page 11 with the following amended paragraph:

In various embodiments, hardware accelerator **506** may include an address generator equipped to generate access addresses for accessing a unit of data in a non-sequential access manner, e.g. a zig-zag pattern. The address generator is the subject matter of co-pending application number<to be inserted>10/651,379, entitled “Non-sequential Access Pattern Based Address Generator”, contemporaneously filed with the present application.